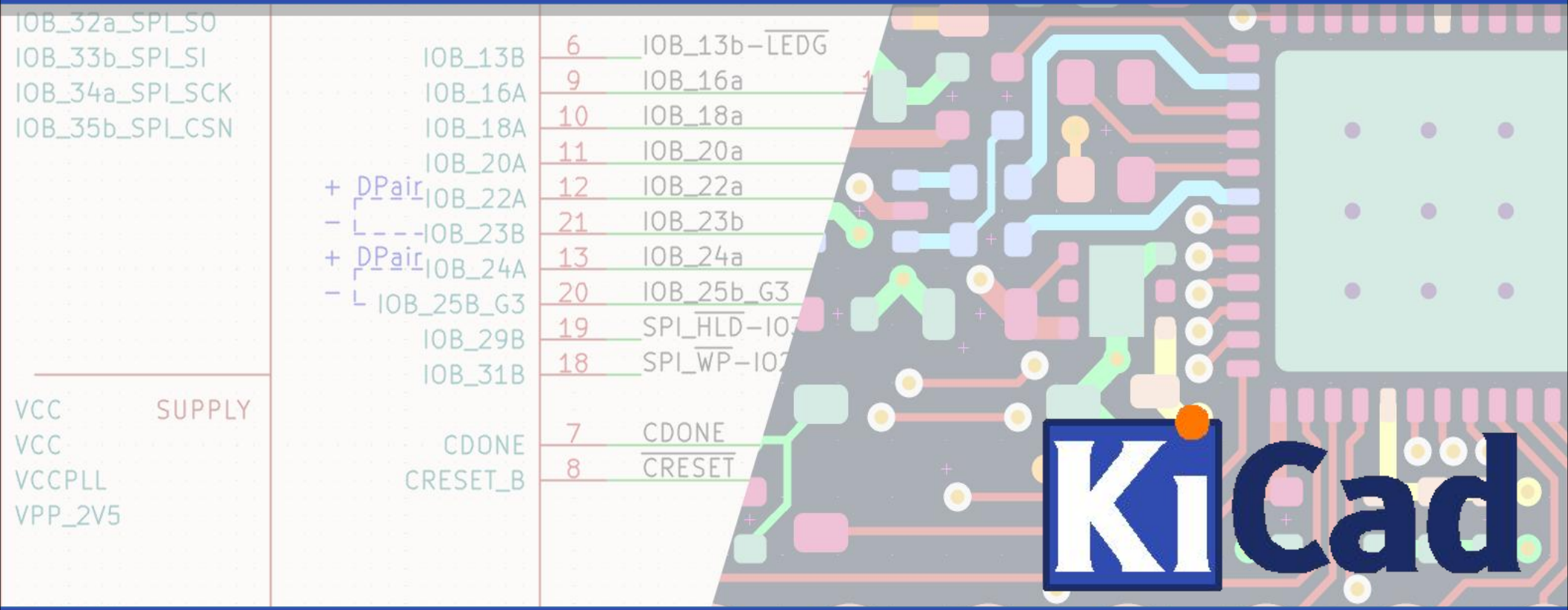
KiCad Project Status

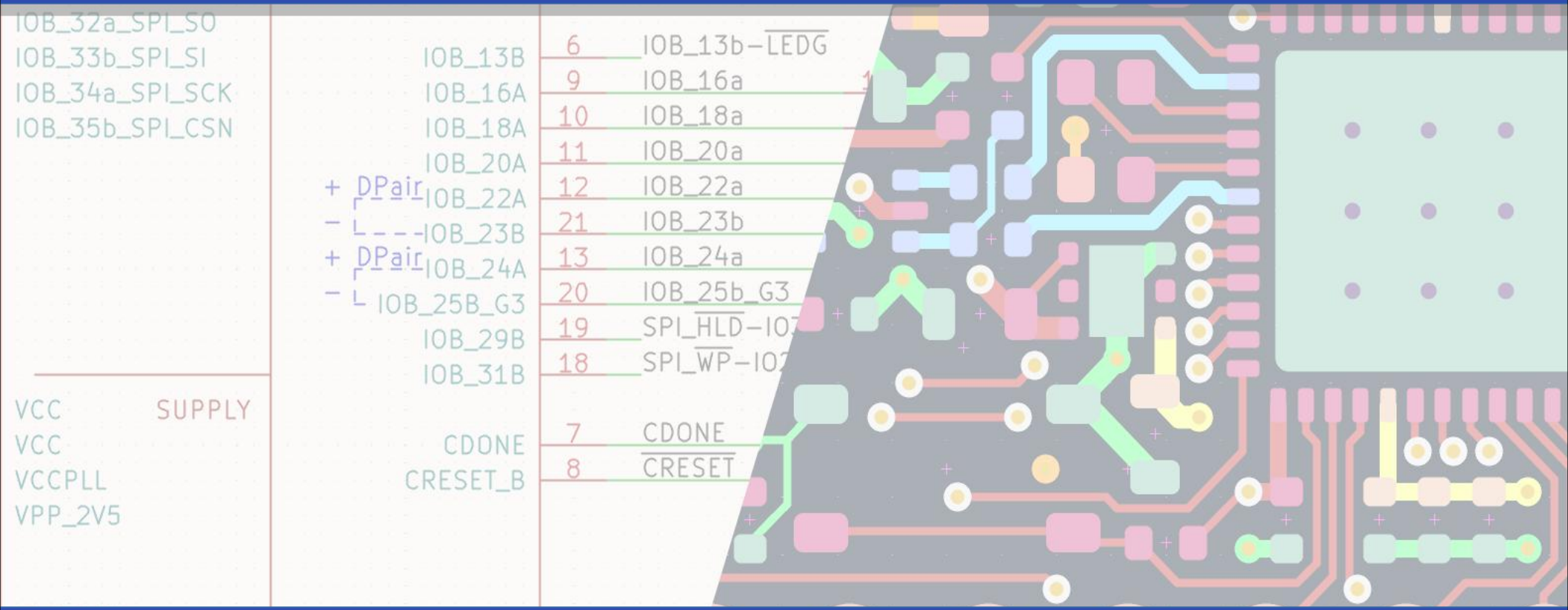


Seth Hillbrand

KiCon Asia 2025

11/14/2025

Project News



- Stable version 9 released February 19th, 2025!
- Bug fix 9.0.6 released October 30th, 2025.
- Stable version 10.0.0 expected to be released January 31st, 2026.
- KiCon US in San Diego in May, 2025
- Third annual KiCon Europe in Bochum, Germany in September, 2025.
- Third annual KiCon Asia in Shenzhen, China in November, 2025.

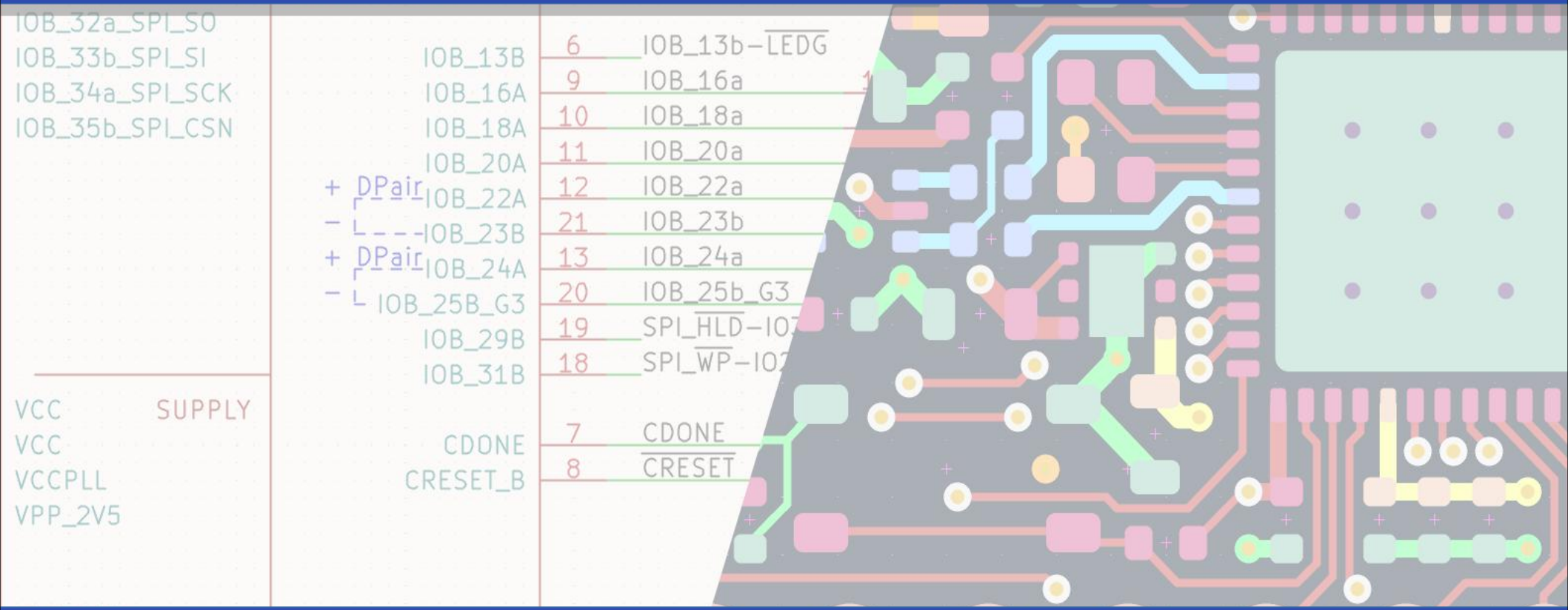
- Thirteen new library team members.
- Three KiCad conferences annually (US, Europe, and Asia).
- KiCad store is now open.
- Three platinum, one gold, five silver, and nine bronze sponsors.
- Follow version 10 development on the KiCad User Forum.

HQ NextPCB specializes in reliable multilayer PCB manufacture and assembly. NextPCB is working with KiCad to provide smart tools to simplify the progression from design to physical product.

KiCad Services Corporation provides direct support to professional KiCad users. They also donate directly to KiCad via the year-end matching grant.

PCBWay specializes in high quality PCB production and turnkey PCB assembly services. They produce 2-14 layers standard PCB, advanced PCB, flex and flex-rigid PCB. They also offer 3D & CNC service.

Version 10


IOB_32a_SPI_SO
IOB_33b_SPI_SI
IOB_34a_SPI_SCK
IOB_35b_SPI_CSN
VCC SUPPLY
VCC
VCCPLL
VPP_2V5
IOB_13B
IOB_16A
IOB_18A
IOB_20A
+ DPair IOB_22A
− IOB_23B
+ DPair IOB_24A
− IOB_25B_G3
IOB_29B
IOB_31B
CDONE
CRESET_B
6 IOB_13b−LEDG
9 IOB_16a
10 IOB_18a
11 IOB_20a
12 IOB_22a
21 IOB_23b
13 IOB_24a
20 IOB_25b_G3
19 SPI_HLD−IO
18 SPI_WP−IO
7 CDONE
8 CRESET

- Setup wizard.
- Chained library tables.
- Arrange arrays by selection order.
- Hatch fills for shapes.
- Arbitrary table rotation angles.
- Altium project importer.
- Custom toolbar layouts.
- 45° full screen cursor.
- Dialog undo/redo.
- Free form (lasso) select.

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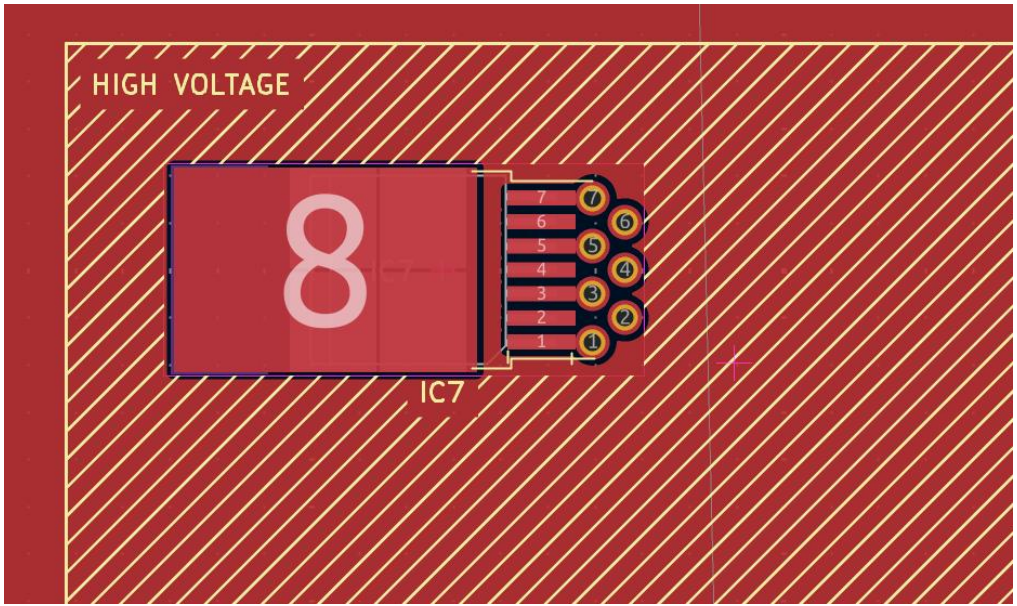

Available in all editors. Supports hatch, reverse hatch, and cross hatch.

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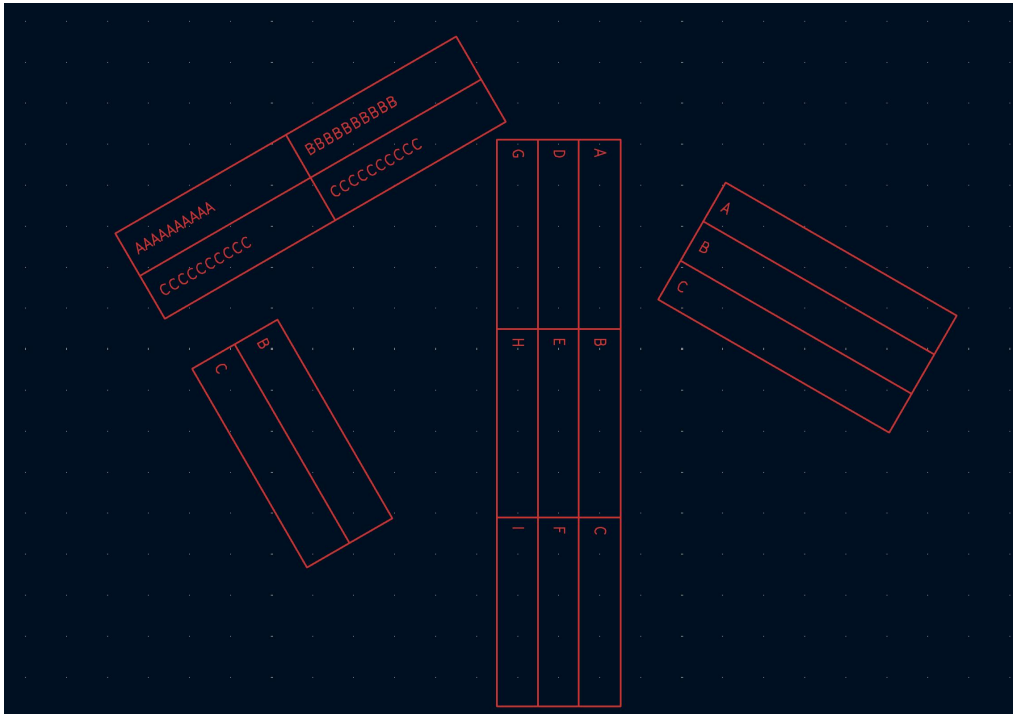

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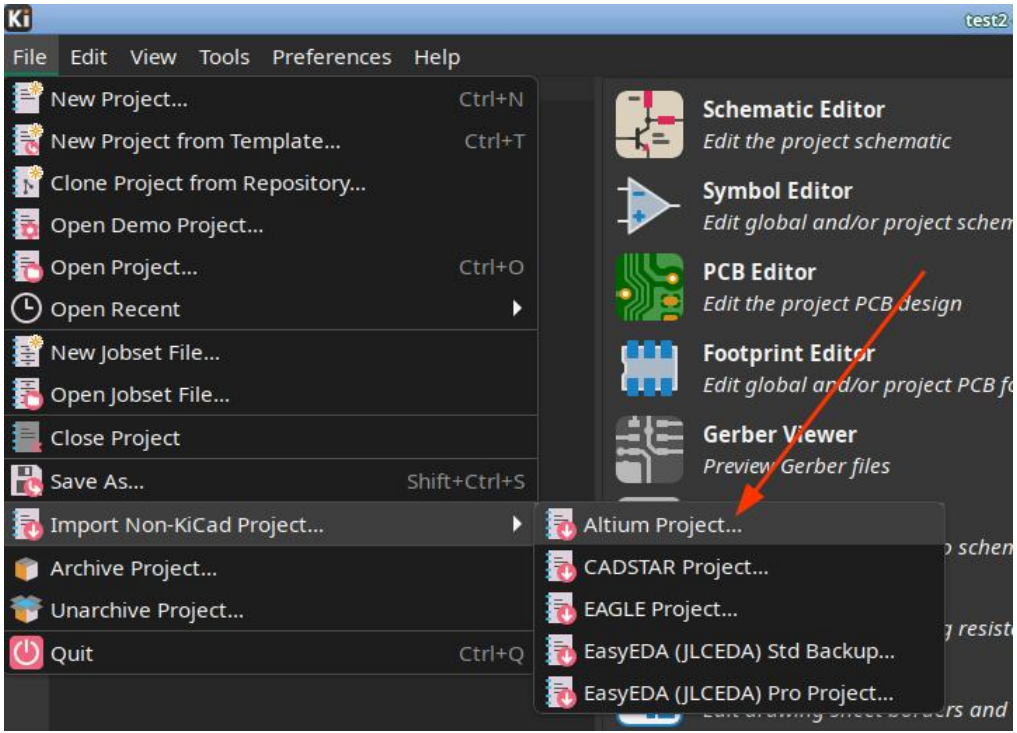

Version 10 Common Features

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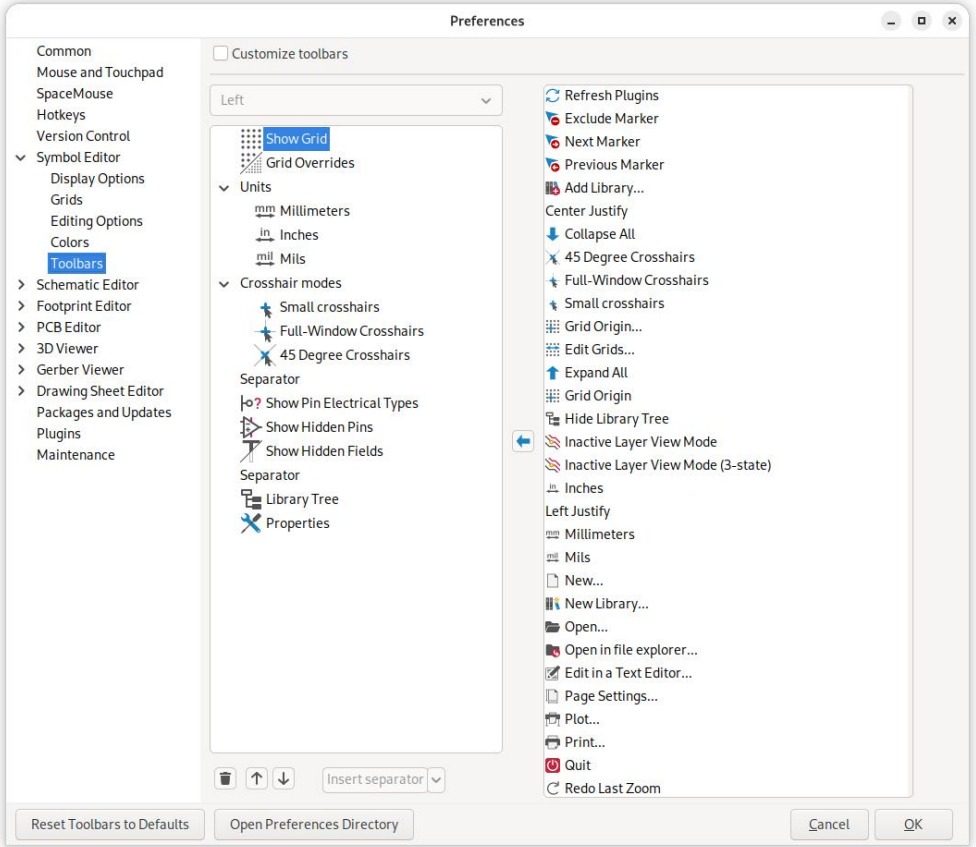

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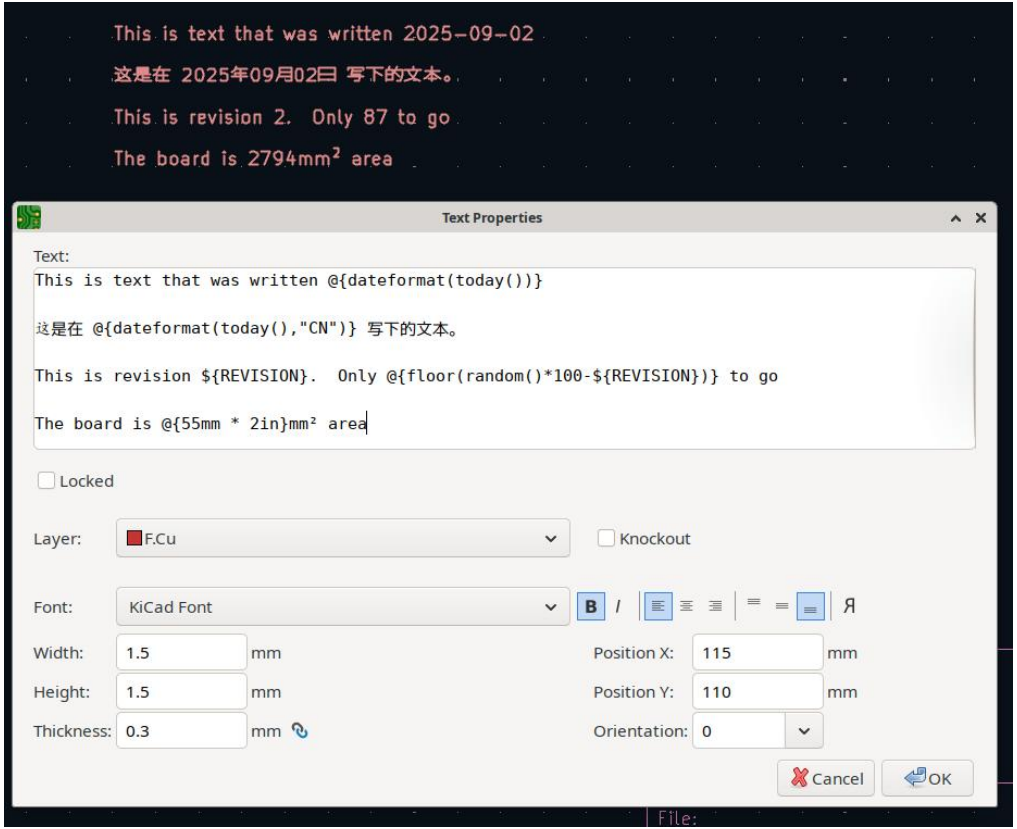
- Expression evaluation of arbitrary text.
- Font selector preview.
- Native rounded rectangles.



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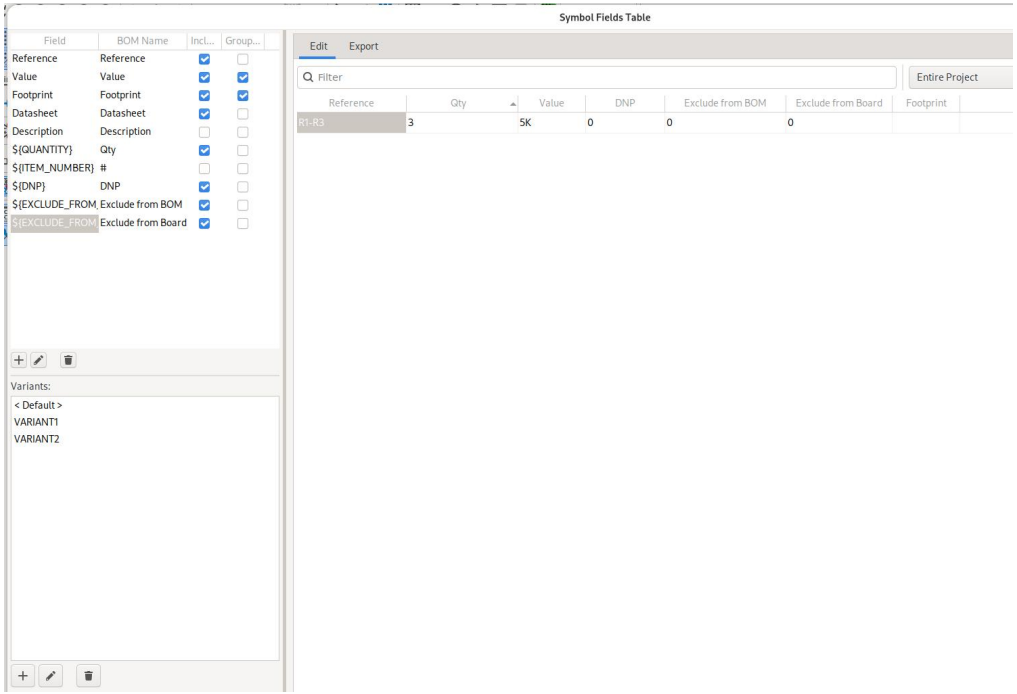

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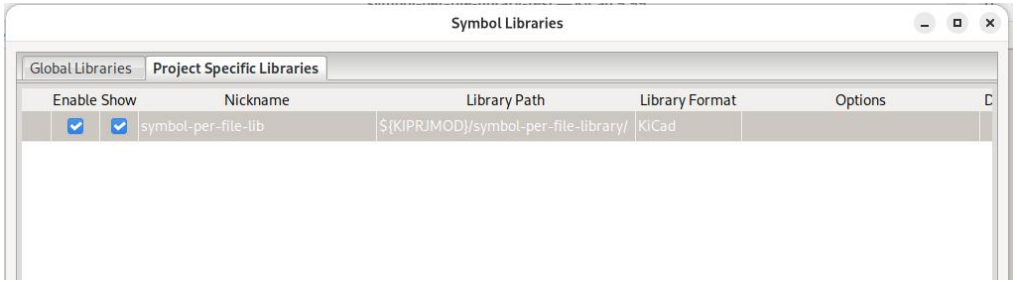

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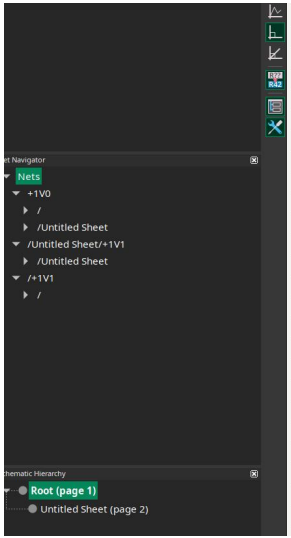

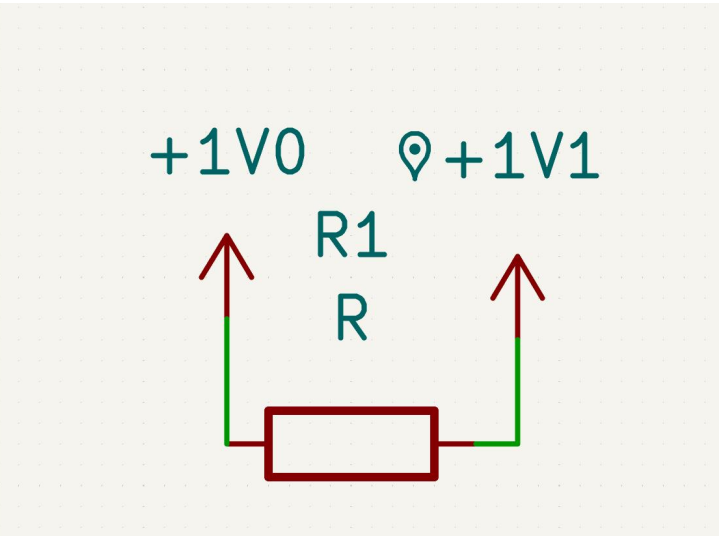

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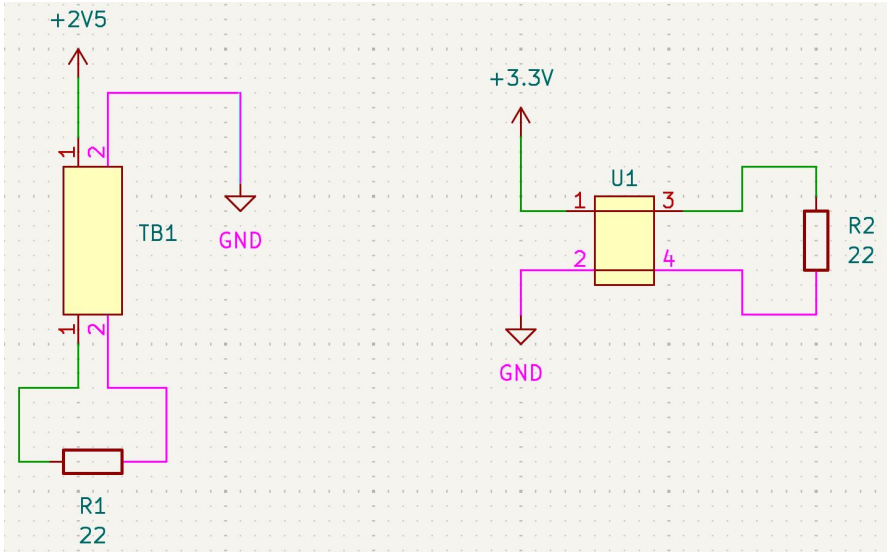

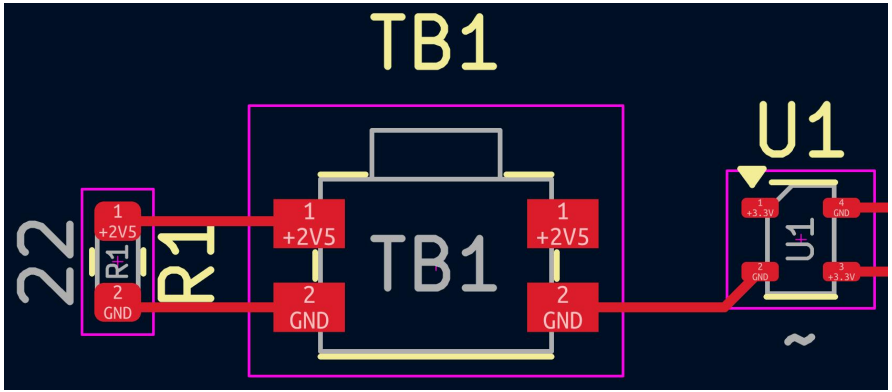

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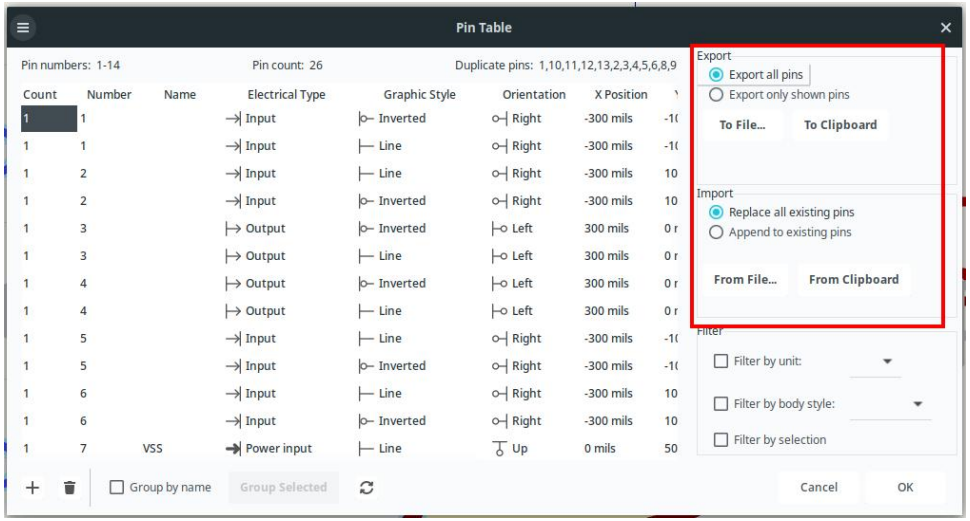

	A	B	C	D	E	F	G	H	I	J	K	L	M
1	Number	Name	Electrical Typ	Graphic Style	Orientation	Number Text	Name Text S	Length	X Position	Y Position	Visible	Unit	De Morgan
2	1		Input	Line	Right	50	50	170	-300	-100	TRUE	A	Standard
3	2		Input	Line	Right	50	50	170	-300	100	TRUE	A	Standard
4	3		Output	Inverted	Left	50	50	150	300	0	TRUE	A	Standard
5	1		Input	Inverted	Right	50	50	150	-300	-100	TRUE	A	Alternate
6	2		Input	Inverted	Right	50	50	150	-300	100	TRUE	A	Alternate
7	3		Output	Line	Left	50	50	150	300	0	TRUE	A	Alternate
8	5		Input	Line	Right	50	50	170	-300	-100	TRUE	B	Standard
9	6		Input	Line	Right	50	50	170	-300	100	TRUE	B	Standard
10	4		Output	Inverted	Left	50	50	150	300	0	TRUE	B	Standard
11	5		Input	Inverted	Right	50	50	150	-300	-100	TRUE	B	Alternate
12	6		Input	Inverted	Right	50	50	150	-300	100	TRUE	B	Alternate
13	4		Output	Line	Left	50	50	150	300	0	TRUE	B	Alternate
14	8		Input	Line	Right	50	50	170	-300	-100	TRUE	C	Standard
15	9		Input	Line	Right	50	50	170	-300	100	TRUE	C	Standard
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18	9		Input	Inverted	Right	50	50	150	-300	100	TRUE	C	Alternate
19	10		Output	Line	Left	50	50	150	300	0	TRUE	C	Alternate
20	12		Input	Line	Right	50	50	170	-300	-100	TRUE	D	Standard
21	13		Input	Line	Right	50	50	170	-300	100	TRUE	D	Standard
22	11		Output	Inverted	Left	50	50	150	300	0	TRUE	D	Standard
23	12		Input	Inverted	Right	50	50	150	-300	-100	TRUE	D	Alternate
24	13		Input	Inverted	Right	50	50	150	-300	100	TRUE	D	Alternate
25	11		Output	Line	Left	50	50	150	300	0	TRUE	D	Alternate
26	14	VDD	Power input	Line	Down	50	50	200	0	-500	TRUE	E	ALL
27	7	VSS	Power input	Line	Up	50	50	200	0	500	TRUE	E	ALL

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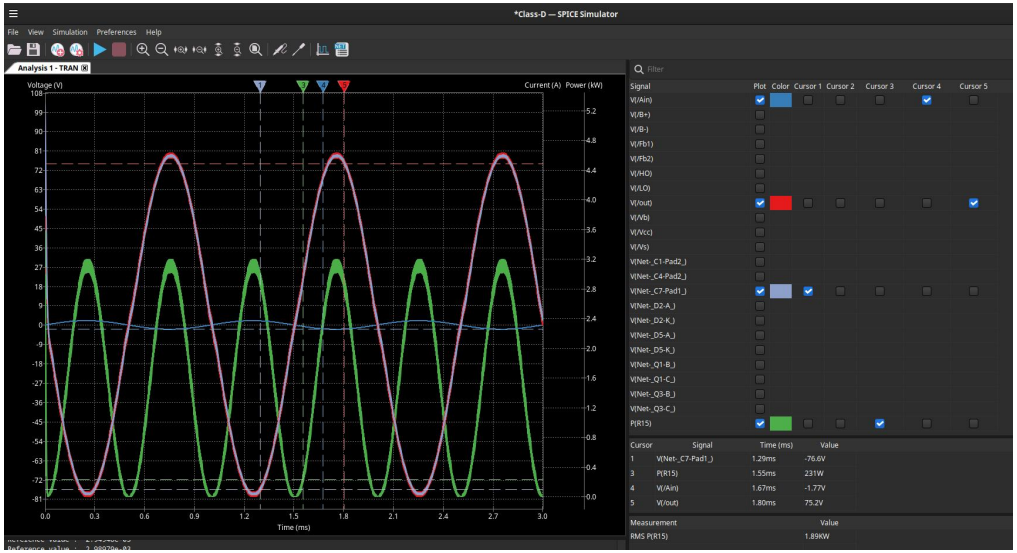

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- Drag & drop images.
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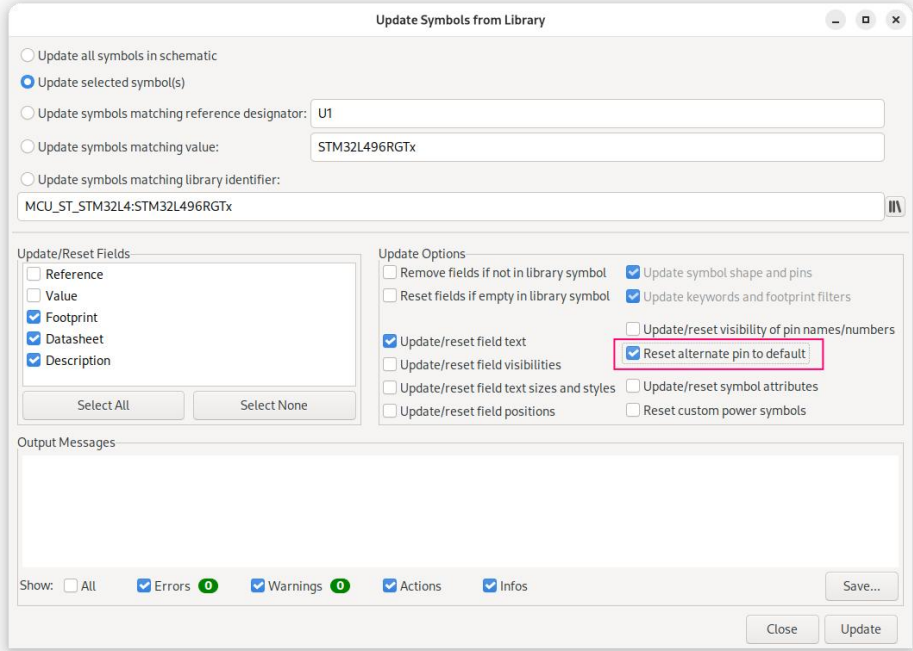

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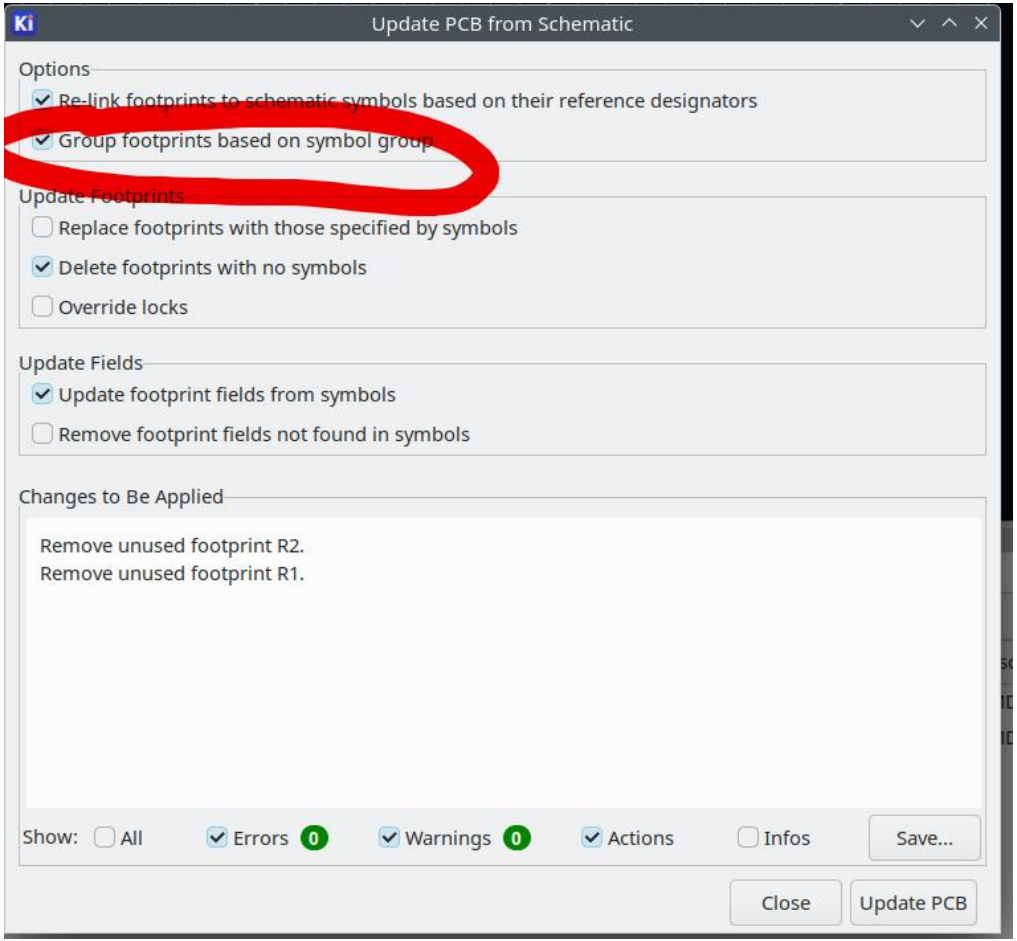

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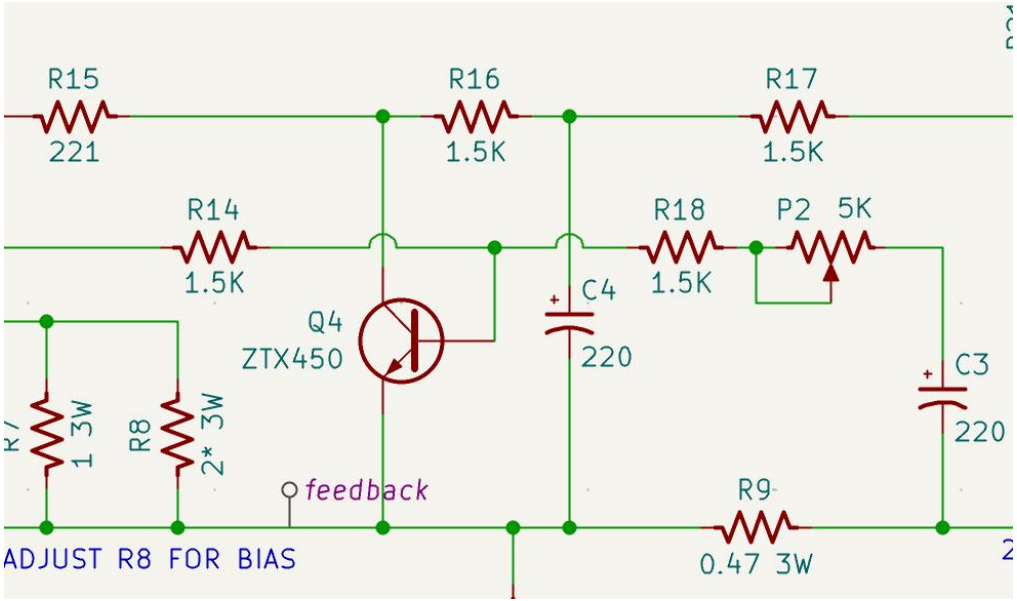

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- Multiple simulation tuning plots.
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- Single file footprint libraries.
- Route multiple tracks.
- Dynamically assign component classes.
- Measurement tool in footprint chooser.
- Unified tuning length code.
- Time/propagation delay length tuning.
- IPC-4761 via protections features.
- Show selected copper object area.
- bridged_mask custom DRC constraint.
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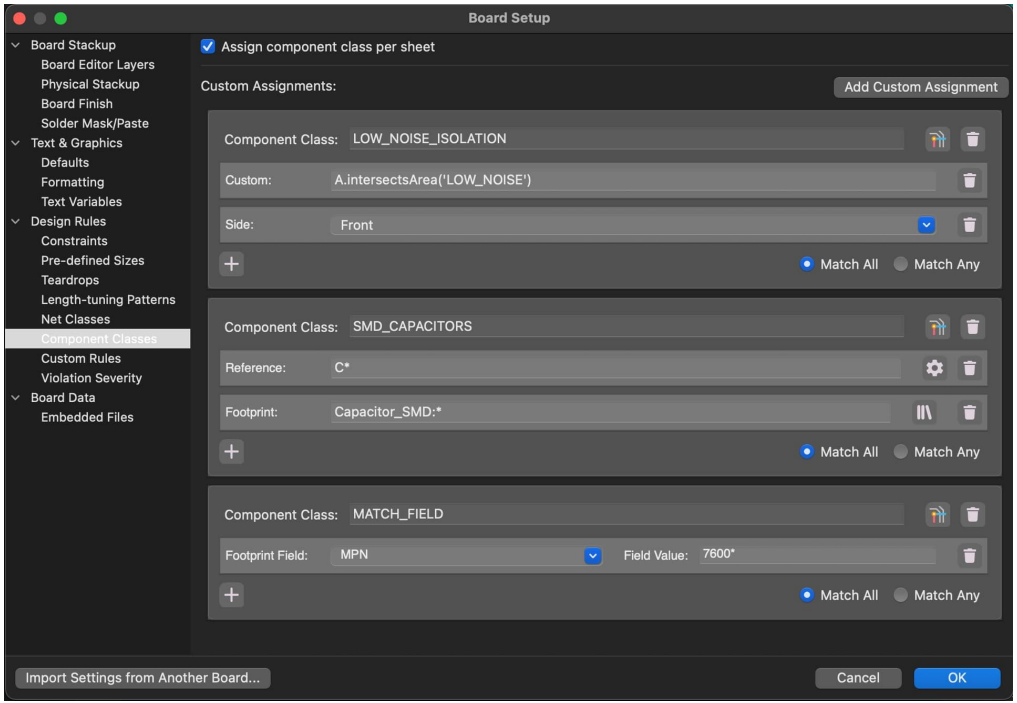

Matching conditions can be reference, footprint library name, orientation, board side, schematic source symbol, or custom DRC rule.

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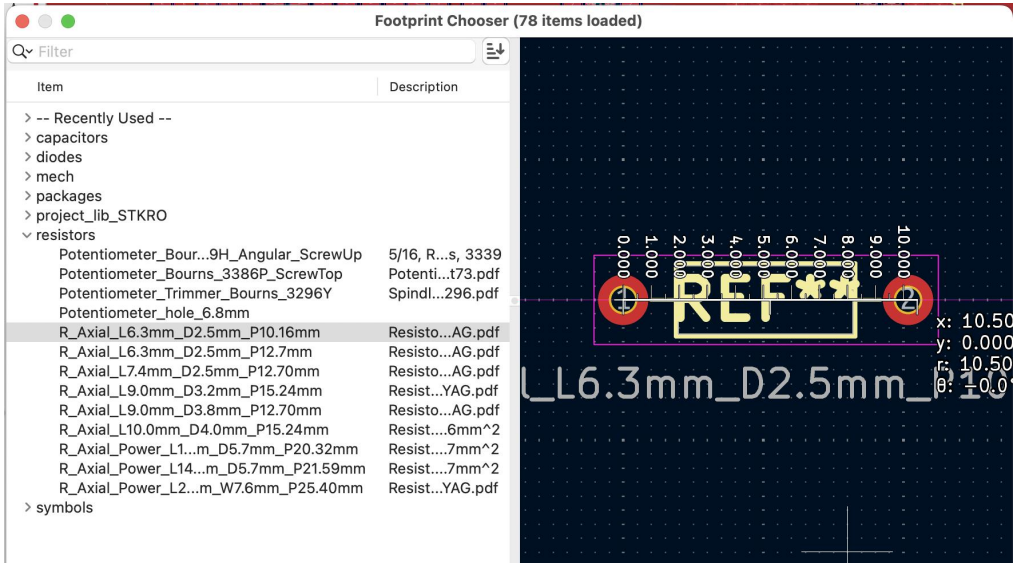

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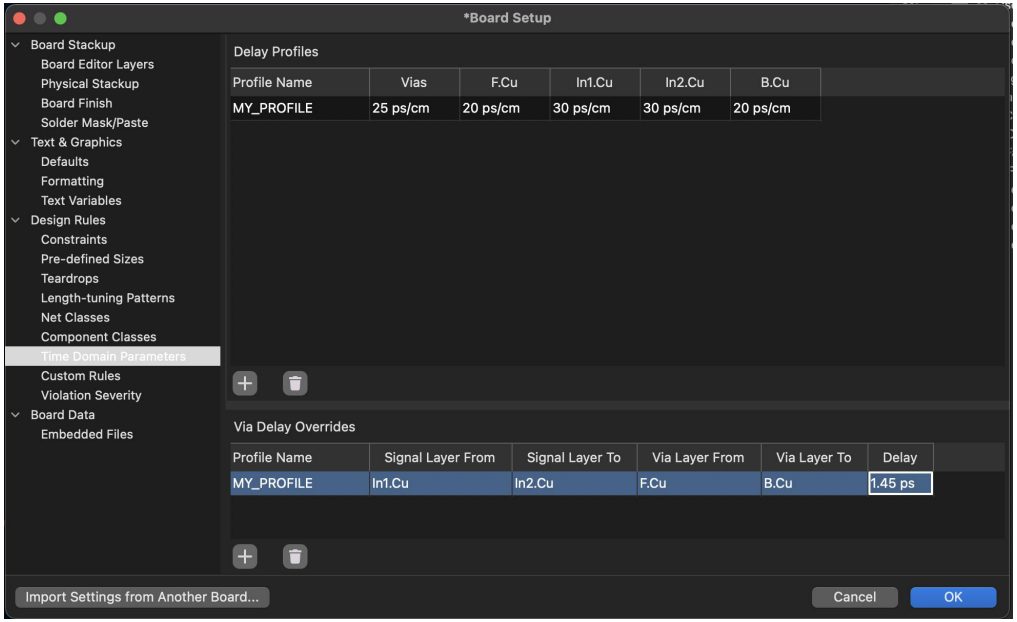

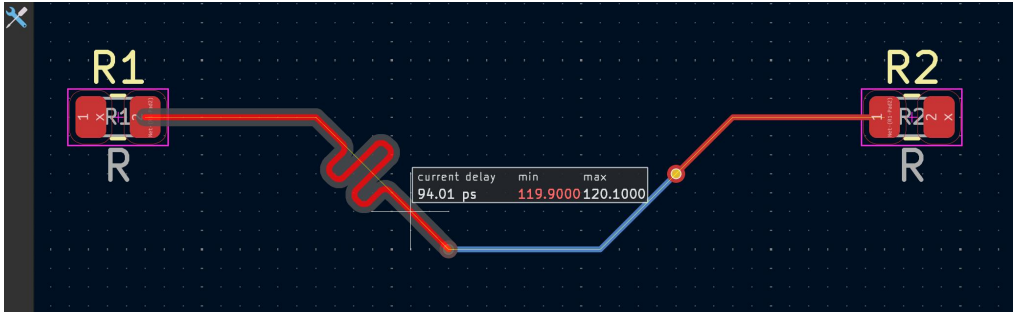

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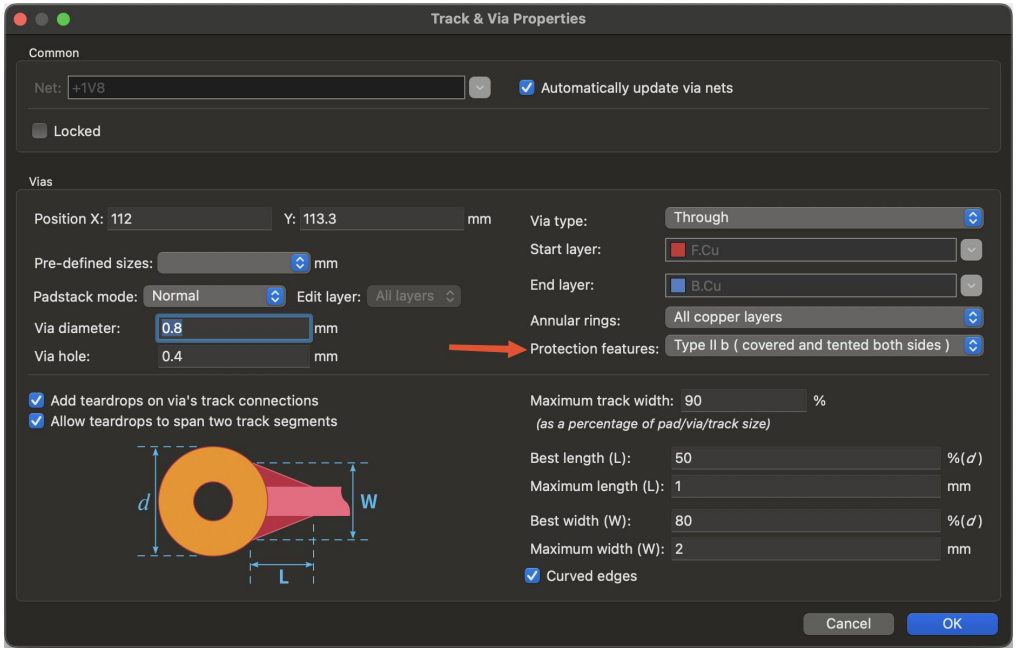

Export to drill file, IPC-2581, and ODB++.

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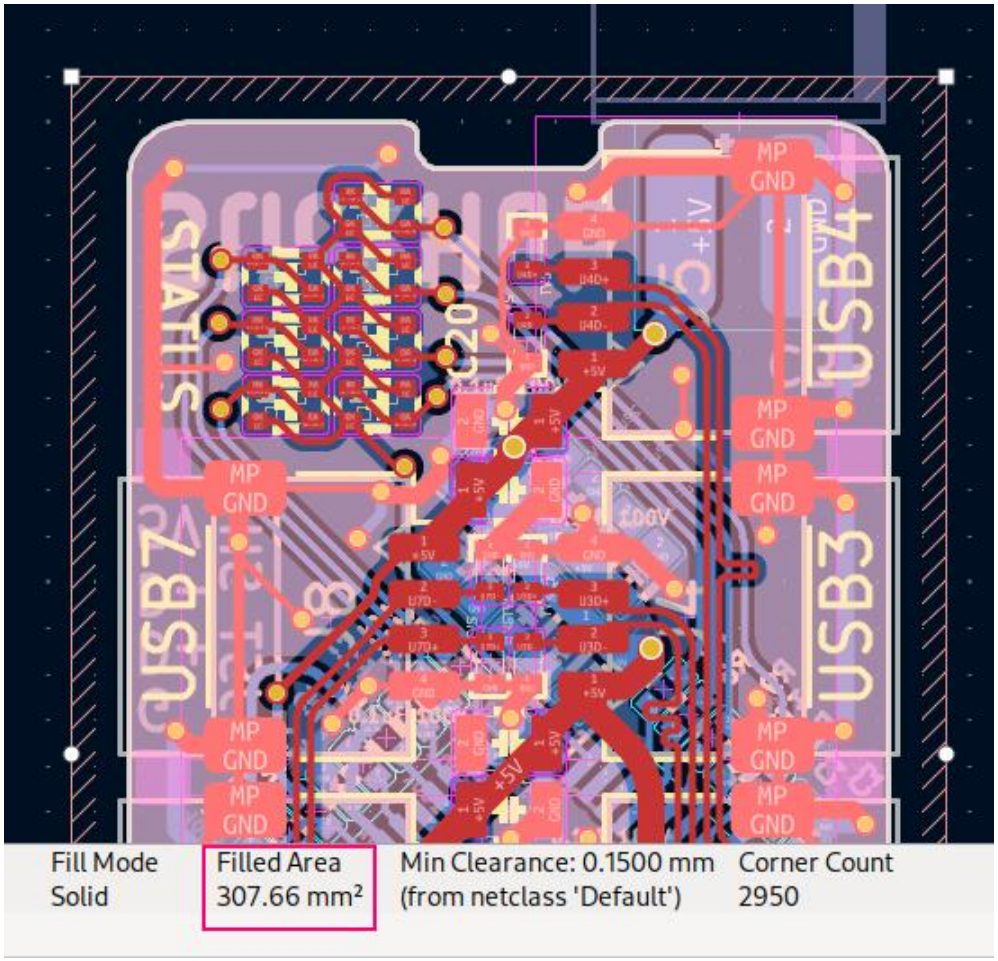

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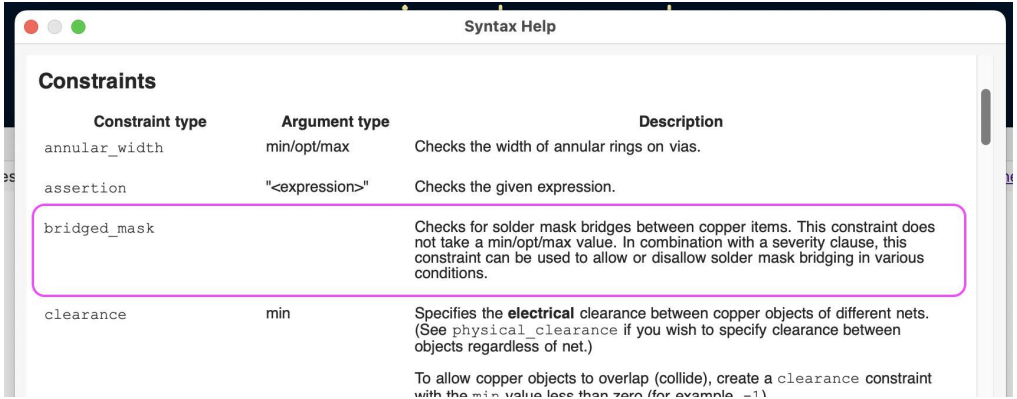

- Single file footprint libraries.
- Route multiple tracks.
- Dynamically assign component classes.
- Measurement tool in footprint chooser.
- Unified tuning length code.
- Time/propagation delay length tuning.
- IPC-4761 via protections features.
- Show selected copper object area.
- bridged_mask custom DRC constraint.
- Precise zone and polygon point editing.

- Remove route by segment.
- Custom solder mask expansion and solder paste margin rules.
- Footprint inner layer objects.
- Actual size (1:1) zoom.
- Filter selection blocking indicator.
- Net selection in properties panel.
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```
# No solder mask expansion for vias.
(rule "no mask expansion on vias"
    (constraint solder_mask_expansion (opt 0mm))
    (condition "A.type == via"))


# Remove solder paste from DNP footprints.
(rule remove_solder_paste_from_DNP
    (constraint solder_paste_abs_margin (opt -50mm))
    (condition "A.Do_not_Populate"))
```

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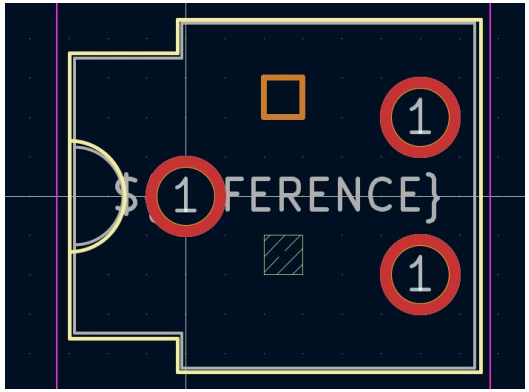

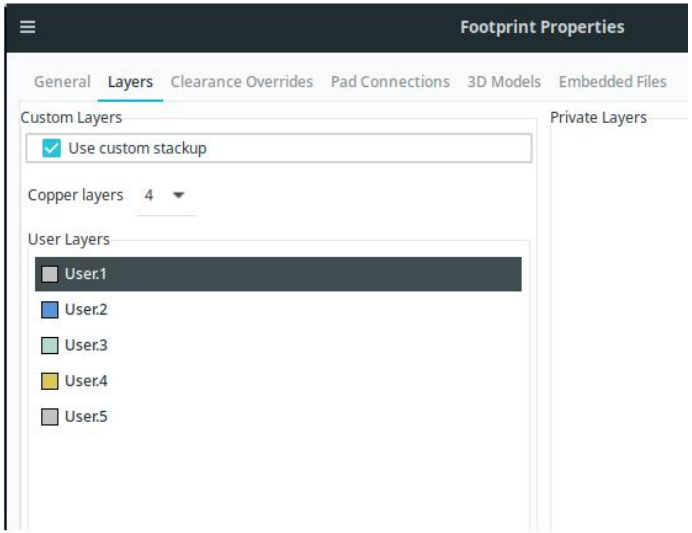

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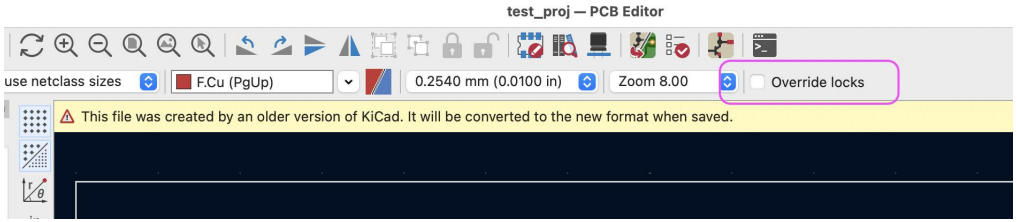

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- Design blocks.
- Drill tab in search panel.
- Bar code support.
- Unconstrained gate swap.
- DRC fix actions.

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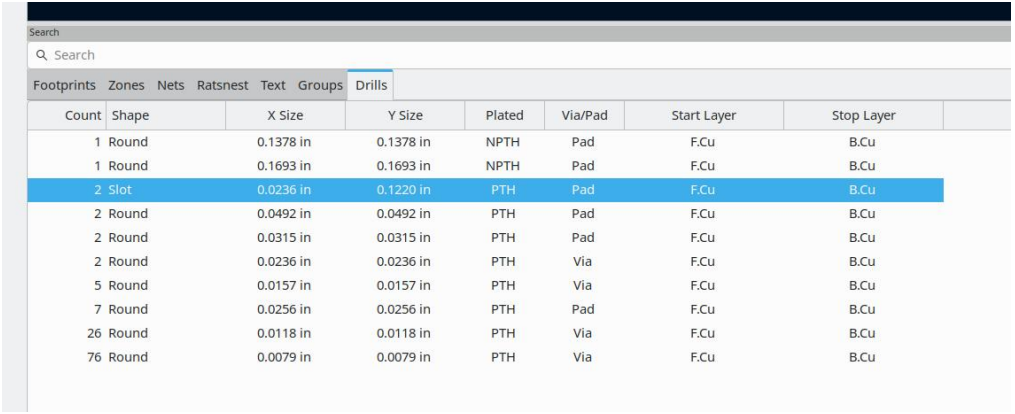

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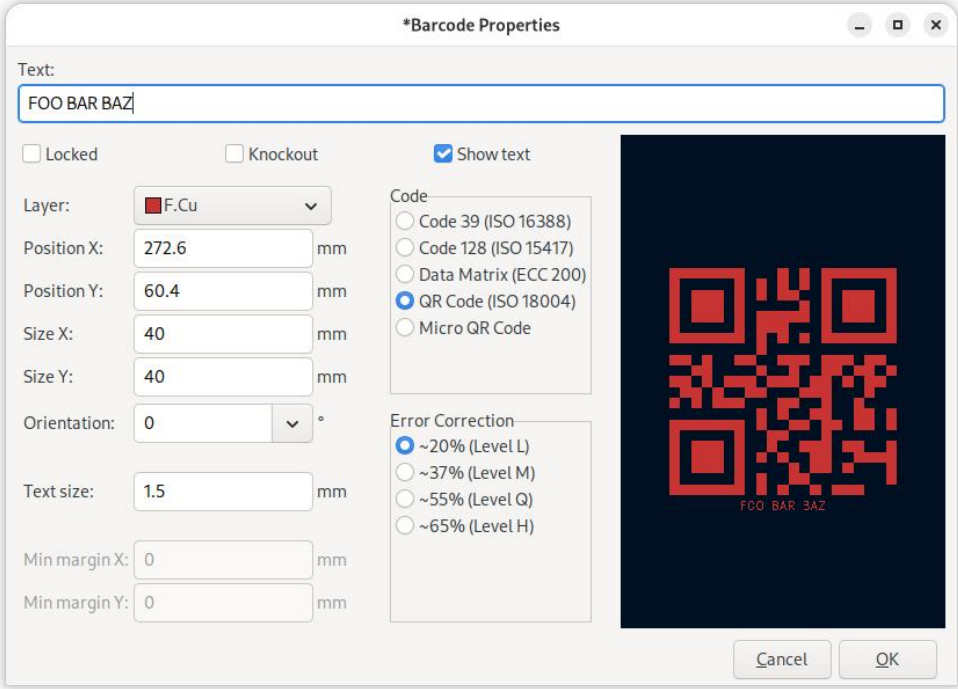

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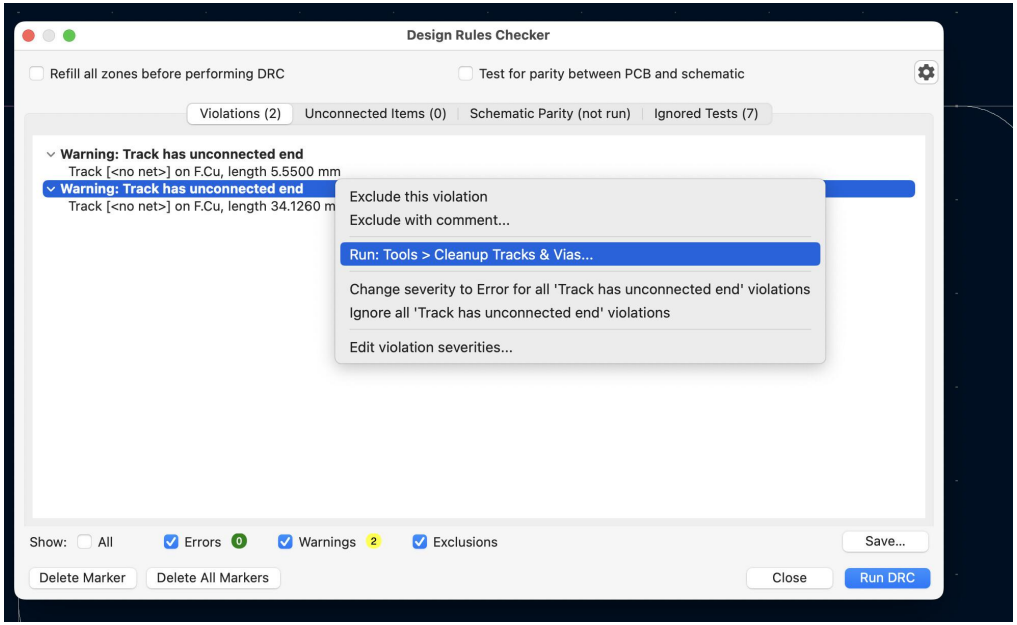
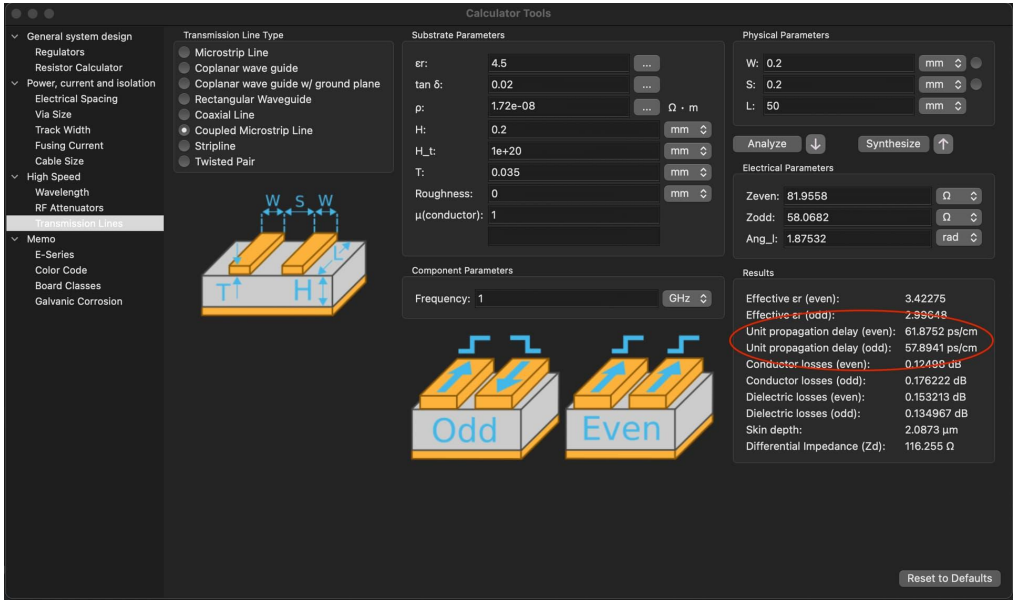


- Calculate propagation delays.
- Calculate symmetric coupled strip lines.
- Windows dark mode.
- Space mouse support for Linux.
- 3D PDF export.



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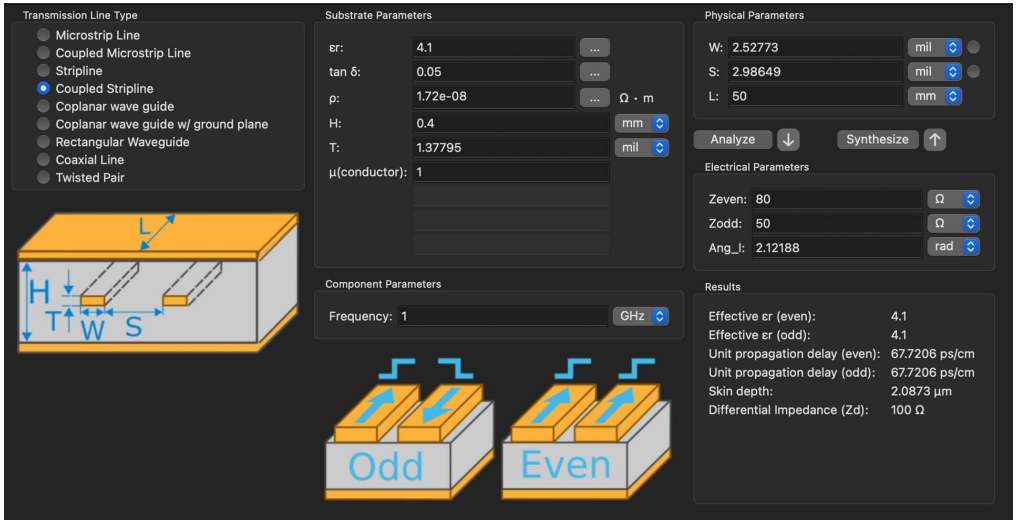

Version 10 Miscellaneous Features

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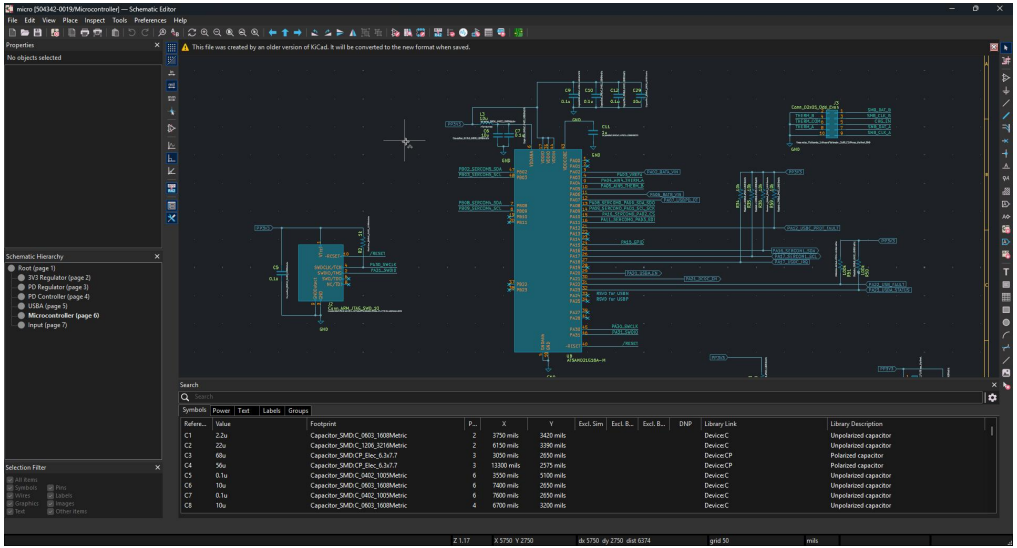

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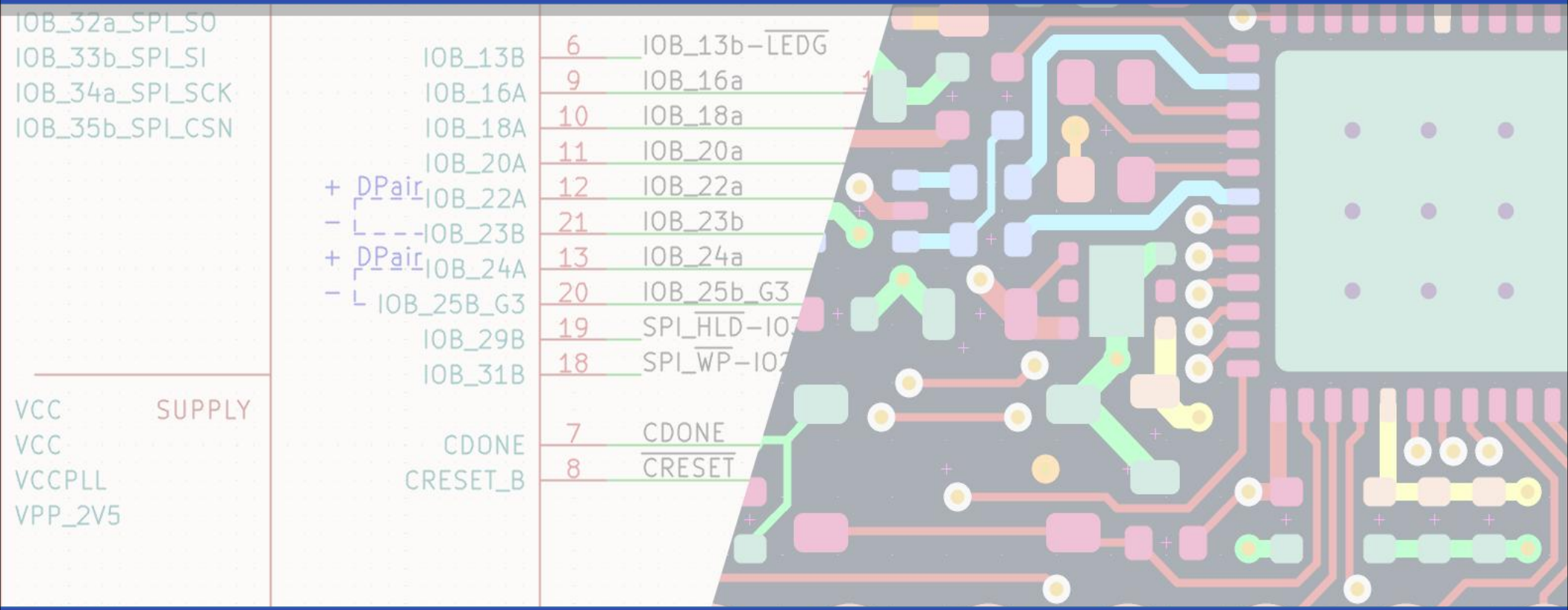
Wrap Up


IOB_32a_SPI_SO
IOB_33b_SPI_SI
IOB_34a_SPI_SCK
IOB_35b_SPI_CSN
VCC SUPPLY
VCC
VCCPLL
VPP_2V5
IOB_13B
IOB_16A
IOB_18A
IOB_20A
+ DPair IOB_22A
− IOB_23B
+ DPair IOB_24A
− IOB_25B_G3
IOB_29B
IOB_31B
CDONE
CRESET_B
6 IOB_13b−LEDG
9 IOB_16a
10 IOB_18a
11 IOB_20a
12 IOB_22a
21 IOB_23b
13 IOB_24a
20 IOB_25b_G3
19 SPI_HLD−IO
18 SPI_WP−IO
7 CDONE
8 CRESET

Thanks to all of the developers who contribute their valuable time and talent to the KiCad project.

Thanks to our event sponsors HQ.

Thank you to all of our sponsors and everyone who has generously donated to the KiCad project.

Thank you for your interest and continued support.

Special thanks to Hubert Hu for organizing KiCon Asia 2025.

Hope to see all of you at a KiCad conference in 2026.